3-14-00 13,



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kie Y. Ahn et al.

Title:

METHODS FOR MAKING INTEGRATED-CIRCUIT WIRING FROM COPPER, SILVER, GOLD, AND

OTHER METALS

Docket No.:

303.648US1

Filed:

January 18, 2000

Examiner:

Serial No.: 09/484,303 Due Date: July 18, 2000

Group Art Unit: 2825

Commissioner for Patents Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

X A return postcard.

X An Information Disclosure Statement (1 pgs.), Form 1449 (9 pgs.), and copies of 125 cited references.

X A Communication Concerning Co-Pending Applications (2 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described above, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this // day of July, 2000.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

Customer Number 21186

(GENERAL)

Atry: Eduardo E. Drake

Reg. No. 40,594

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IN THE UNITED STATES RATES TAND TRADENTARK OFFICE

JUL 1 2 2000

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FED-CIRCUIT WIRING FROM COPPER, SILVER, GOLD, AND OTHER METALS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants further request that a copy of the 1449 form, initialed by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication.

Under 37 C.F.R. §1.97(b)(3), it is believed that no fee or certificate is required with this Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge any additional fees or credit any overpayment to Account No. 19-0743.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

KIE Y. AHN ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 349-9**59**3

Eduardo E/Drake

Reg. No. 40,594

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner of Patents, Washington, D.C. 20231 on July 2, 3000.

Name

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<u>S/N 09/484,303</u> PATENT

IN THE UNITED STATES PATELLAND TRADEMARK OFFICE

Applicant:

Kie Y. Ahn et al.

Serial No.:

09/484,303

Filed: Title:

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METHODS FOR MAKINGINTEG

JUL 1 2 2000 1

Examiner:

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Docket: 303.648US1

TED-CIRCUIT WIRING FROM

COPPER, SILVER, GOLD, AND OTHER METALS

COMMUNICATION CONCERNING CO-PENDING APPLICATION(S)

Commissioner for Patents Washington, D.C. 20231

Applicant would like to bring to the Examiner's attention the following related copending application(s) in the above-identified patent application:

<u>Serial No.</u> 09/483,881	Filing Date 01/18/2000	Attorney Docket 00303.672US1	Title SELECTIVE ELECTROLESS-PLATED COPPER METALLIZATION
09/483,869	01/18/2000	00303.664US1	STRUCTURES AND METHODS TO ENHANCE COPPER METALLIZATION
09/488,098	01/18/2000	00303.618US1	METHOD AND APPARATUS FOR MAKING INTEGRATED-CIRCUIT WIRING FROM COPPER, SILVER, GOLD, AND OTHER METALS
09/484,002	01/18/2000	00303.673US1	PROCESS FOR PROVIDING SEED LAYERS FOR USING ALUMINUM, COPPER, GOLD AND SILVER METALLURGY
09/032,197	02/27/1998	00303.459US1	METHODS FOR MAKING COPPER AND OTHER METAL INTERCONNECTIONS IN INTEGRATED CIRCUITS
09/128,859	08/04/1998	00303.473US1	COPPER METALLURGY IN INTEGRATED CIRCUITS
09/145,012	09/01/1998	00303.505US1	INTEGRATED-CIRCUIT WITH OXIDATION-RESISTANT POLYMERIC RESISTANT POLYMERIC ROLL IN ALL ROLL ROLL ROLL ROLL ROLL ROLL ROLL

Page 2 Dkt: 303.648US1

Serial Number: 09/484,303

Filing Date: January 18, 2000

METHODS FOR MAKING INTEGRATED-CIRCUIT WIRING FROM COPPER, SILVER, GOLD, AND OTHER METALS

09/256,123 02/24/1999 00303.505US2 FORMING SUBMICRON INTEGRATED-CIRCUIT WIRING FROM GOLD, SILVER, COPPER AND OTHER **METALS** FORMING SUBMICRON 09/256,124 02/24/1999 00303.505US3 INTEGRATED-CIRCUIT WIRING FROM GOLD, SILVER, COPPER AND OTHER **METALS** MULTILEVEL COPPER 09/584,157 05/31/2000 00303.685US1 INTERCONNECT WITH DOUBLE **PASSIVATION**



Respectfully submitted,

KIE Y. AHN ET AL.

By Applicant's Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 349-9583

Eduardo E. Drake

Reg. No. 40,594

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner of Patents, Washington, D.C. 20231 on July 2000.

Name

Signature